

5/Election
1-7-02
R. DudaPATENTAttorney Docket No. 018865-005100
Client Ref: 17732-19420

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

R. Joshi

Application No.: 09/731,226

Filed: December 4, 2000

For: **PASSIVATION SCHEME FOR BUMPED
WAFERS**

Examiner: Hunk K. Vu

Art Unit: 2811

RESPONSE TO RESTRICTION
REQUIREMENT

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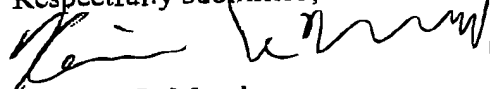
TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the restriction requirement mailed December 5, 2001, applicant elects to prosecute Claims 1-3, drawn to a semiconductor device. Applicant reserves the right to prosecute Claims 4-5, drawn to a method of making a semiconductor device in a separate application..

Respectfully submitted,



Kevin T. LeMond

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Atty Docket No. 018865-005100US

PTO FAX NO.: 1 703 308-7722

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Group Art Unit 2811

OFFICIAL COMMUNICATION
FOR THE PERSONAL ATTENTION OF**EXAMINER Hunk K. Vu**

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CERTIFICATION OF FACSIMILE TRANSMISSION

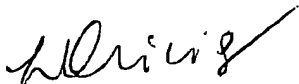
I hereby certify that the following document(s) in re Application of Rajeev Joshi et al., Application No. 09/731,226, filed December 4, 2000 for PASSIVATION SCHEME FOR BUMPED WAFERS is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Document(s) Attached

1. Response to Restriction Requirement

Number of pages being transmitted, including this page: 2

Dated: 01/03/02



Lata Olivier

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